

Electronic Acknowledgement Receipt

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Title of Invention:	Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)		
1		P6447_RFOA_061107.pdf	283972 876771781e4dfc5f8c05018406bde73f7f64474a	yes	8		
	Multipart Description/PDF files in .zip description						
	Document Description		Start	End			
	Amendment After Final		1	1			
	Claims		2	4			
	Applicant Arguments/Remarks Made in an Amendment		5	8			
Warnings:							
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